

**REMARKS**

The Office Action of **March 18, 2002**, has been received and its contents carefully noted. Concurrently filed herewith is a *Request for a One (1) Month Extension of Time* that extends the shortened statutory period for response until **July 18, 2002**. Accordingly, Applicant respectfully submits that this response is timely filed and fully responsive to the Office Action.

Claims 1-16 were pending the present application prior to the above amendment. By the above amendment, claims 1, 6, 11 and 14 are amended and new claims 17-26 are added. Please note that the amendments to claims 1, 6, 11 and 14 were merely for the purpose of correcting grammatical errors. Applicant submits that no issue of new matter is set forth by this amendment. Accordingly, claims 1-26 are currently pending in this application and are believed to be in condition for allowance for the reasons stated below.

**A. Information Disclosure Statement**

In further support of Applicant's *Request for Acknowledgment of Information Disclosure Statement*, which was filed April 5, 2002, Applicant respectfully requests that the Examiner evidence consideration of Japanese patent publication JP 07-130652 ( which was cited in the IDS of November 3, 2000) by providing an initialed copy of the Form PTO-1449.

**B. 35 U.S.C. 102 Rejection**

Claims 1, 2, 4-7 and 9-16 stand rejected under 35 U.S.C. 102(e) as anticipated by U.S. Patent No. 6,078,317 to Sawada. Applicant respectfully traverses this rejection for at least the following reasons.

In accordance with claims 1, 2, 4-7 and 9-16, the claimed invention is directed generally to a display device and a method for operating a display device including, *inter alia*, a display panel, an image signal processing circuit for processing an image signal input from an external source, and a control circuit for controlling the display panel and the image signal processing circuit.

**Suwada Fails to Teach the Claimed Invention**

Applicant respectfully contends that Sawada clearly fails to teach or inherently describe each and every element defined by the claimed invention. For instance, the Examiner finds that Sawada teaches “a control circuit in the form of a display mode dependence controller (17).”

On the other hand, the method and apparatus in accordance with the claimed invention requires a control circuit that controls the display panel and the image signal processing circuit. Specifically, as illustrated in Fig. 1 and expressly recited on lines 13-15 of page 6 of the specification, “the control circuit 170 controls various kinds of signals to be fed to the liquid crystal display panel 100 and the image signal processing circuit 160.”

The Examiner fails to indicate whether such a feature is expressly taught by Sawada. It should be noted, however, that Applicant’s review of Sawada finds that the display mode dependence controller (17) taught therein does not control the ferroelectric liquid crystal display (4), a feature that is required in the display device of the claimed invention.

Thus, insofar as the display mode dependence controller (17) of Sawada fails to control the ferroelectric liquid crystal display (4), it is believed that the display device of the claimed

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(directly or  
indirectly)

invention is clearly patentably distinct from the display device of Sawada. In view of the foregoing remarks, reconsideration and withdrawal of the rejection is earnestly solicited.

**C. 35 U.S.C. 103 Rejection**

Claims 3 and 8 stand rejected under 35 U.S.C. 103(a) as unpatentable over Sawada in view of U.S. Patent No. 6,008,801 to Jeong. Applicant respectfully traverses this rejection for at least the following reasons.

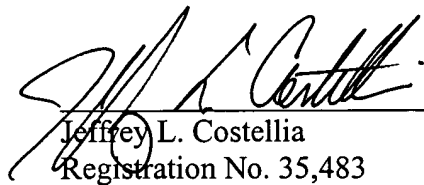
**The Proposed Combination Fails to Disclose the Claimed Invention**

It is contended that insofar as the base reference Sawada fails to expressly teach or implicitly suggest each and every feature presently set forth in base claims 1 and 6 of the present invention, claims 3 and 8 cannot be rendered *prima facie* obvious since they incorporate by reference each and every feature set forth in claims 1 and 6, respectively. In particular, Sawada fails to disclose or suggests a display device comprising a control circuit that controls both a display panel and an image signal processing circuit, as required by claims 3 and 8. Moreover, the secondary reference Jeong fails to modify Sawada in a manner sufficient to render the claimed invention obvious since it too fails to disclose a control circuit that controls both a display panel and an image signal processing circuit. In view of the foregoing remarks, reconsideration and withdrawal of the rejection is earnestly solicited.

**Conclusion**

Having responded to all of the rejections set forth in the Office Action, it is submitted that the claims are now in condition for allowance. An early and favorable Notice of Allowance is respectfully solicited. In the event that the Examiner is of the opinion that a brief telephone or personal interview will facilitate allowance of one or more of the above claims, the Examiner is courteously requested to contact Applicants' undersigned representative.

Respectfully submitted,



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**MARKED-UP VERSION OF AMENDMENT**

1. (Amended) A display device comprising:

a display panel comprising a pixel portion in which a plurality of thin film transistors are arranged in a matrix, a source driver circuit, and a gate driver circuit;

an image signal processing circuit for processing an image signal input from an external source; and

a control circuit for controlling said display panel and said image signal processing circuit,

wherein said image signal processing circuit corrects said image signal on a basis of a correction table and feeds said display panel with said corrected image signal.

6. (Amended) A display device comprising:

a display panel comprising a pixel portion in which a plurality of thin film transistors are arranged in a matrix, a source driver circuit, and a gate driver circuit;

an image signal processing circuit for processing an image signal input from an external source; and

a control circuit for controlling said display panel and said image signal processing circuit,

wherein said image signal processing circuit performs gamma correction on said image signal on a basis of a correction table and feeds said display panel with said corrected image signal on which gamma correction has been performed.

11. (Amended) A method for operating a display device comprising the steps of:  
processing an image signal input from an external source by an image signal processing  
circuit;

controlling said image signal processing circuit and a display panel by a control circuit;  
correcting said image signal based on a correction table; and  
supplying a corrected image signal to said display panel through a correction circuit.

14. (Amended) A method for operating a display device comprising the steps of:  
processing an image signal input from an external source by an image signal processing  
circuit;

controlling said image signal processing circuit and a display panel by a control circuit;  
performing a gamma correction of said image signal based on a correction table; and  
supplying a corrected image signal to said display panel through a correction circuit.